



10G SFP+ 10G-BX(BR)-D Compact 2CH Bidi Transceiver Hot Pluggable, Dual Bidi LC, Tx1330nm DFB / Rx1270nm, SMF 10KM, DDM

Part Number: FSPP-HC-C33-10D



Overview

FSPP-HC-C33-10D Small Form Factor Pluggable CSFP transceivers are compliant with the current SFP+ Multi-Source Agreement (MSA) Specification. It achieves operational compatibility with conventional SFP+ and no damage to CSFP/ host board if CSFP transceiver is plugged into a conventional SFP+ socket. The high performance uncooled 1330nm DFB transmitter and high sensitivity PIN receiver provide superior performance for 10GBASE Ethernet applications up to SMF 10km optical links.

Applications

- 10GBASE-BX(BR) Ethernet @10.3125G
- Fiber Channel 800-SM-LC-L 8GFC @8.5G, 1200-SM-LL-L 10GFC @10.51875G
- SONET OC-192 & SDH STM-64 @9.953G
- CPRI Option #7 @9.83G, #8 @10.1376G

Features

- Compatible with IEEE802.3ae 10GBASE-BX(BR)
- Compliant with CPRI Option 7, 8
- Compliant with SFF-8431, SFF-8432 SFP+ MSA
- Support 8.5Gb/s to 10.51875Gb/s Multi-Rate
- Compliant with CSFP MSA Option 2
- Hot Pluggable
- 2CH Bidi unit with 1330nm DFB laser transmitter and 1270nm PIN receiver
- Dual Bidi LC connector
- 2-wire interface for management and diagnostic monitor compliant with SFF-8472
- Single +3.3V power supply
- Link distance 10km over SM fiber
- RoHS Compliant

Laser Safety

- This is a Class 1 Laser Product complies with 21 CFR 1040.10 and 1040.11 except for conformance with IEC 60825-1 Ed. 3., as described in Laser Notice No. 56, dated May 8, 2019.
- Caution: Use of control or adjustments or performance of procedure other than those specified herein may result in hazardous radiation exposure.



Absolute Maximum Ratings

Parameters	Symbol	Min.	Max.	Unit
Storage Temperature	T _{ST}	-40	+85	°C
Storage Relative Humidity	RH	5	85	%
Supply Voltage	V _{CC}	-0.5	+4.0	V

Recommended Operating Conditions

Parameters	Symbol	Min.	Typ.	Max.	Unit
Case Operating Temp. (FSPP-HC-C33-10D)	T _{OP}	0	-	+70	°C
Case Operating Temp. (FSPP-HC-C33-10Di)	T _{OP}	-40	-	+85	°C
Supply Voltage	V _{CC}	+3.13	+3.3	+3.47	V
Supply Current (FSPP-HC-C33-10D)	I _{CC}			600	mA
Supply Current (FSPP-HC-C33-10Di)	I _{CC}			700	mA

Transmitter Electro-optical Characteristics

V_{CC}= 3.13V to 3.47V, T_{OP} = 0 °C to 70 °C (FSPP-HC-C33-10D); T_{OP} = -40 °C to 85 °C (FSPP-HC-C33-10Di)

Parameters	Symbol	Min.	Typ.	Max.	Unit	Note
Operating Data Rate	DR	8.5	10.3125	10.5	Gb/s	
Optical Launch Power	P _o	-5		+2	dBm	1
Optical Center Wavelength	λ _c	1320	1330	1340	nm	
Spectral Width (-20dB)	Δλ			1	nm	
Side Mode Suppression Ratio	SMSR	30			dB	
Optical Extinction Ratio	ER	3.5			dB	
Optical Eye Mask		IEEE802.3ae				
Differential Data Input Swing	V _{IN}	180		850	mV	
Tx Disable Input Voltage-Low (Tx ON)	TDISV _L	GND		0.8	V	
Tx Disable Input Voltage-High (Tx OFF)	TDISV _H	2.0		V _{CC}	V	
Tx Fault Output Voltage-Low (Tx Normal)	TFLTV _L	GND		0.8	V	
Tx Fault Output Voltage-High (Tx Fault)	TFLTV _H	2.0		V _{CC}	V	

Note1: The optical power is launched into a 9/125μm single mode fiber.



Receiver Electro-optical Characteristics

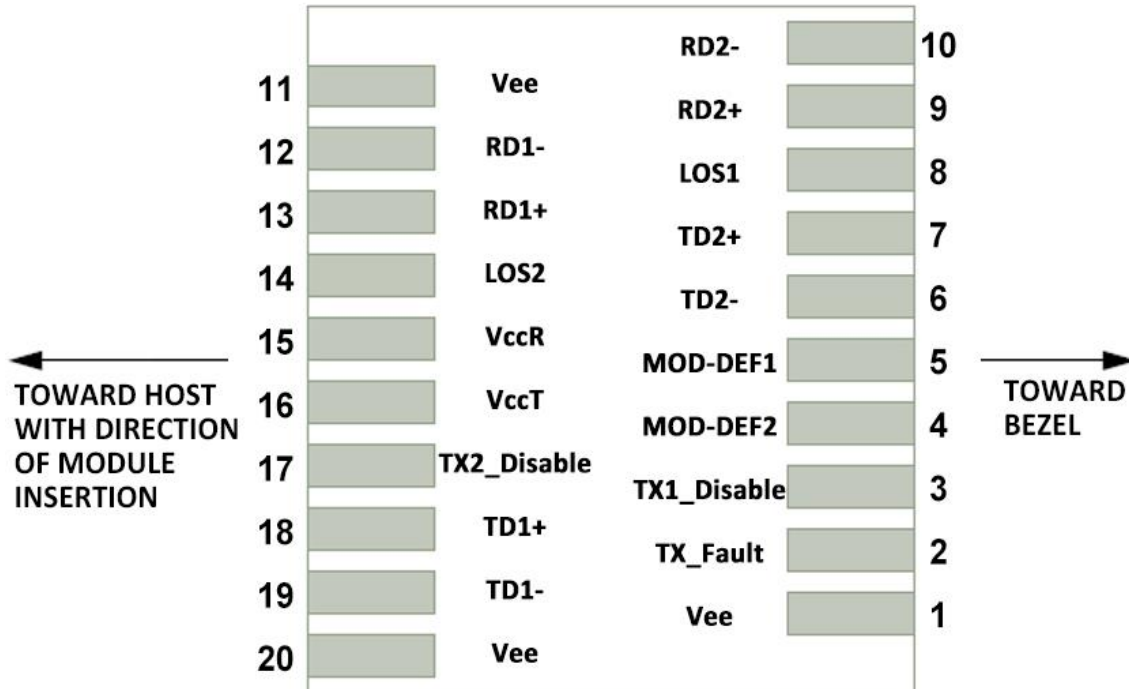
$V_{CC} = 3.13V$ to $3.47V$, $T_{OP} = 0\text{ }^{\circ}C$ to $70\text{ }^{\circ}C$ (FSPP-HC-C33-10D); $T_{OP} = -40\text{ }^{\circ}C$ to $85\text{ }^{\circ}C$ (FSPP-HC-C33-10Di)

Parameters	Symbol	Min.	Typ.	Max.	Unit	Note
Operating Data Rate	DR	8.5	10.3125	10.5	Gb/s	
Receiver Sensitivity	P_{IN_min}			-12	dBm	1
Maximum Input Power	P_{IN_max}	+0.5			dBm	1
Optical Center Wavelength	λ_c	1260	1270	1280	nm	
LOS De-Assert	LOS_D			-14	dBm	
LOS Assert	LOS_A	-30			dBm	
LOS Hysteresis	LOS_{HY}	0.5			dB	
Differential Data Output Swing	V_{OUT}	300		900	mV	
Receiver LOS Signal Output Voltage-Low	LOS_{VL}	GND		0.8	V	
Receiver LOS Signal Output Voltage-High	LOS_{VH}	2.0		V_{CC}	V	

Note1: Measured with a PRBS $2^{31}-1$ test pattern @10.3125Gbps BER< 10^{-12} .



Pin Assignment



Host PCB CSFP Pad Assignment Top View

Pin Description

Pin	Name	Function / Description
1	Vee	Transceiver Ground
2	Tx_Fault	Transmitter Fault Indication (1)
3	Tx1_Disable	Transmitter Disable of CH1 (Turns off transmitter laser output of CH1)
4	MOD_DEF2	2-wire Serial Interface Data Line (SDA)
5	MOD_DEF1	2-wire Serial Interface Clock Line (SCL)
6	TD2-	Inverted Transmit Data Input of CH2
7	TD2+	Transmit Data Input of CH2
8	LOS1	Loss of signal for CH1
9	RD2+	Received Data Output of CH2
10	RD2-	Inverted Received Data Output of CH2
11	Vee	Transceiver Ground
12	RD1-	Inverted Received Data Output of CH1



13	RD1+	Received Data Output of CH1
14	LOS2	Loss of signal for CH2
15	VccR	Receiver Power
16	VccT	Transmitter Power
17	Tx2_Disable	Transmitter Disable of CH2 (Turns off transmitter laser output of CH2)
18	TD1+	Transmit Data Input of CH1
19	TD1-	Inverted Transmit Data Input of CH1
20	Vee	Transceiver Ground

Note1: Tx_Fault is internally OR output for Tx fault conditions in either Channel 1 or Channel 2. In order to determine which channel is at fault, the Host can read the internal memory bits for status:

- Bit2 in (A2h Byte#110) for TX1 fault
- Bit2 in (B2h Byte#110) for TX2 fault

Digital Diagnostic Functions

As defined by the SFP MSA (SFF-8472) Ficer's CSFP transceivers provide digital diagnostic functions via a 2-wire serial interface, which allows real-time access to the following operating parameters:

- Transceiver temperature
- Laser bias current
- Transmitted optical power
- Received optical power
- Transceiver supply voltage

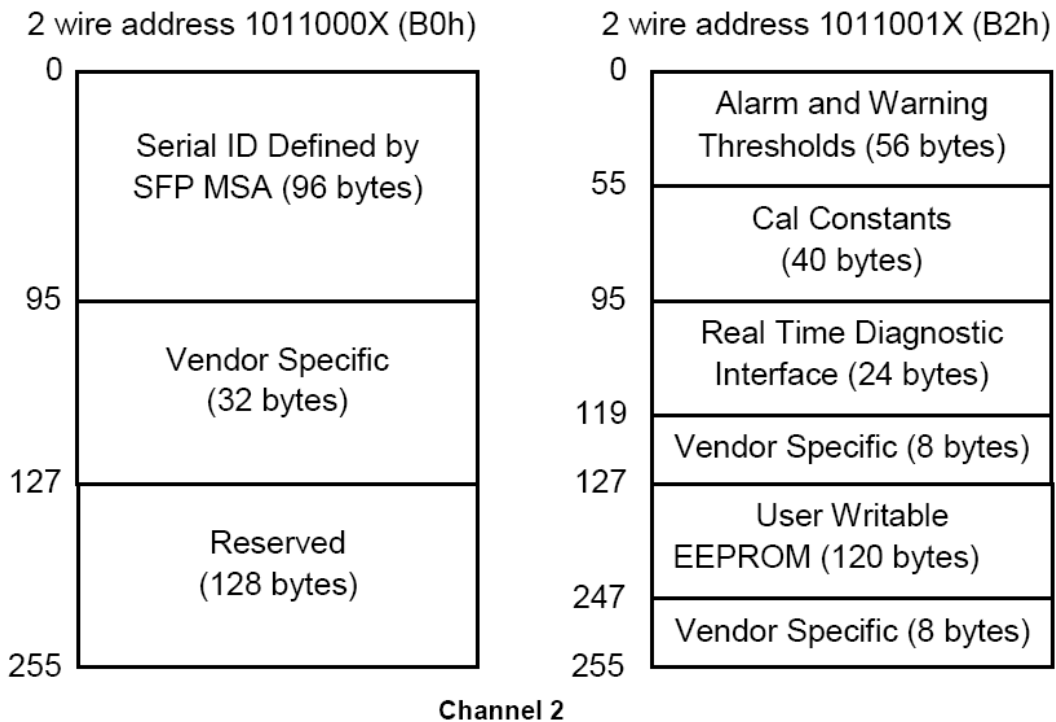
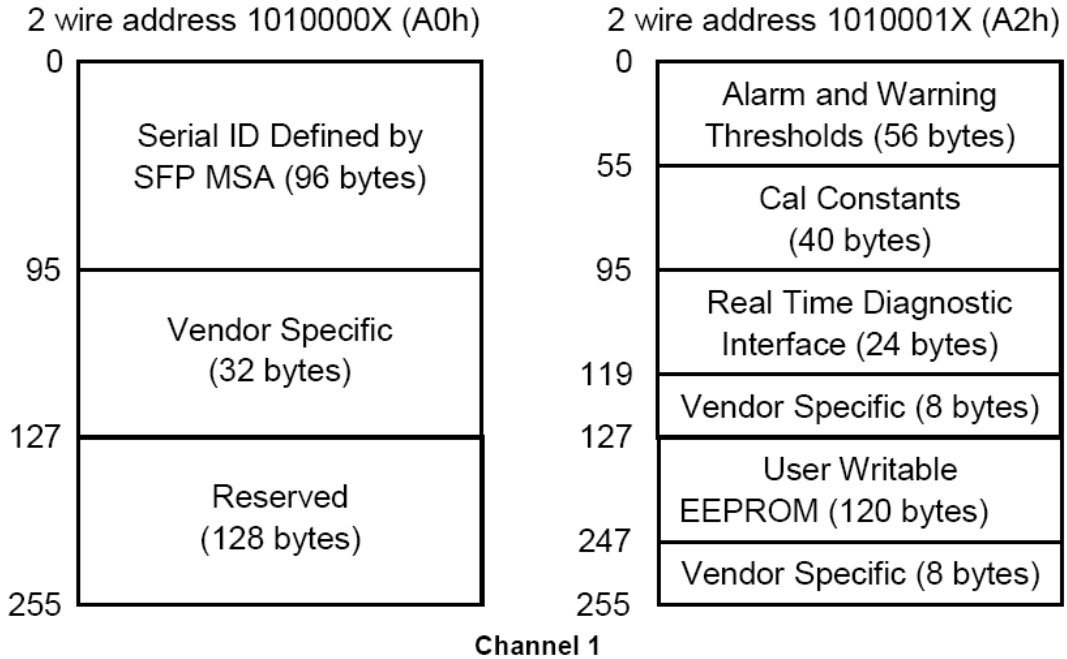
It also provides a sophisticated system of alarm and warning flags, which may be used to alert end-users when particular operating parameters are outside of a factory-set normal range.

The operating and diagnostics information is monitored and reported by a Digital Diagnostics Controller (DDC) inside the transceiver, which is accessed through the 2-wire serial interface. When the serial protocol is activated, the serial clock signal (SCL pin) is generated by the host. The positive edge clocks data into the SFP transceiver into those segments of its memory map that are not write-protected. The negative edge clocks data from the SFP transceiver. The serial data signal (SDA pin) is bi-directional for serial data transfer. The host uses SDA in conjunction with SCL to mark the start and end of serial protocol activation. The memories are organized as a series of 8-bit data words that can be addressed individually or sequentially.

For more detailed information including memory map definitions, please see the SFP MSA (SFF-8472) Specification.



Digital Diagnostic Memory Map



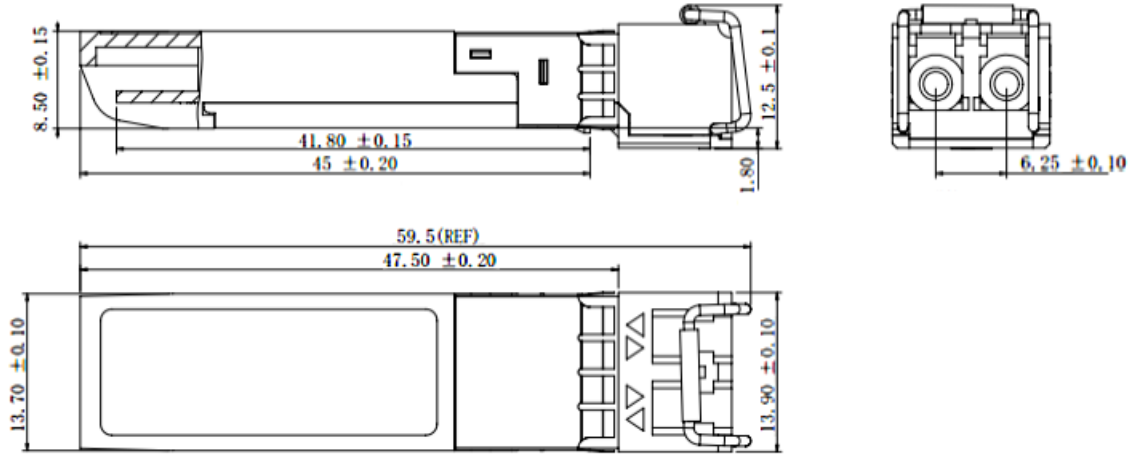


Digital Diagnostic Monitoring Characteristics

Parameter	Accuracy	Unit	Note
Temperature	±3	°C	Internal Calibration
Supply Voltage	±0.1	V	Internal Calibration
Tx Bias Current	±5	mA	Internal Calibration
Tx Output Power	±3	dB	Internal Calibration
Rx Received Optical Power	±3	dB	Internal Calibration



Mechanical Dimensions



(All Dimensions are ± 0.20 mm Unless Otherwise Specified, Unit: mm)

Ordering Information

Part No.	Tx	Rx	Link	DDM	Temp.
FSPC-HC-C33-10D	1330nm	1260nm	SMF 10km	Yes	0~70°C
FSPC-HC-C33-10Di		~ 1280nm			-40~85°C

NOTE: Distances are indicative only. To calculate a more precise link budget based on specific conditions in your application, please refer to the optical characteristics.